PALM INTRANET

Day : Saturday Date: 10/20/2007

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Inventor Name Search Result

Your Search was:

Last Name = MISHRA First Name = SHRIDHAR

Application#	Patent#	Status	Date Filed	Title	Inventor Name
09964204	Not Issued			Testing of SOC	MISHRA, SHRIDHAR MUBARAQ
10065005	7080184	150	09/09/2002	ISDN-BASED BUS INTERFACE	MISHRA, SHRIDHAR MUBARAQ
10526529	Not Issued	30	03/02/2005	Data switch and a method for broadcast packet queue estimation	MISHRA, SHRIDHAR MUBARAQ
10526700	Not Issued	30	03/04/2005	Method and system for determining conformance of a data key with rules by means of memory lookups	MISHRA, SHRIDHAR MUBARAQ
10526811	Not Issued	30	03/04/2005	Intelligent stacked switching system	MISHRA, SHRIDHAR MUBARAQ
10526886	Not Issued	41	03/04/2005	Method and system for controlling bandwidth allocation	MISHRA, SHRIDHAR MUBARAQ
10526992	Not Issued	30	03/07/2005	Parser for parsing data packets	MISHRA, SHRIDHAR MUBARAQ
10526990	Not Issued	30	03/07/2005	Configurable fast ethernet and gigabit ethernet data port	MISHRA, SHRIDHAR MUBARAQ

Inventor Search Completed: No Records to Display.

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Inventor Name Search Result

Your Search was:

Last Name = ARDHANARI First Name = GURUPRASAD

Application#	Patent#	Status	Date Filed	Title	Inventor Name
10526529	Not Issued	30		1	ARDHANARI, GURUPRASAD
10526700	Not Issued	30			ARDHANARI, GURUPRASAD
10526886	Not Issued	41		Method and system for controlling bandwidth allocation	ARDHANARI, GURUPRASAD
11364740	Not Issued	25		Data switch, and communication system using the data switch	ARDHANARI, GURUPRASAD
11674501	Not Issued	20	1	METHOD OF CONTROLLING A DATA FLOW, TRANSMITTER AND DATA TRANSMISSION SYSTEM	ARDHANARI, GURUPRASAD

Inventor Search Completed: No Records to Display.

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